



13th International Workshop on
**CHARACTERIZATION AND
MODELING OF MEMORY DEVICES**
OCTOBER 3-4, 2024 | MILAN, ITALY

PROGRAMME

OCTOBER 3

- 09.30 Registration
- 10.15 Opening
- 10.30 **Robert Haas**, IBM, Zurich
In-memory computing: bringing sustainability to AI
- 11.15 **Onur Mutlu**, ETH, Zurich
Memory-Centric Computing: Enabling Fundamentally More Efficient Computers
- 12.00 **Roberto Bez**, Micron Technology
Semiconductor memories and Paolo Cappelletti: 40 years together
- 12.40 Award Ceremony
- 12.50 Lunch
- 14.00 **Barbara de Salvo**, Meta
On the future of AR wearable platforms: novel technology opportunities and challenges
- 14.45 **Johannes Müller**, Global Foundries
Embedded STT-MRAM: building trust and moving ahead
- 15.45 Coffee Break

OCTOBER 4

- 09.30 **Suman Datta**, Georgia Tech, Atlanta
Logic compatible and fast ferroelectric field effect transistor memory
- 10.15 **Andras Kis**, EPFL, Lausanne
Large-scale integrated circuits with 2D MoS₂ for neuromorphic computing
- 11.00 Coffee Break
- 11.30 **Tibor Grasser**, TU, Wien
Insulators for advanced devices built with 2D semiconductors
- 12.05 **Gaurav Thareja**, Applied Materials
High performance PeriFET for advanced DRAM devices enabled by materials, process and systems co-optimization
- 12.35 **Catherine Dubourdieu**, Freie Universität Berlin
CMOS back-end-of-line compatible ferroelectric tunnel junctions and ferroelectric field-effect transistors
- 13.10 Lunch

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